

mSYS-3500

Flexible, Modular and Scalable mTCA.4
Open Standard Compliant System

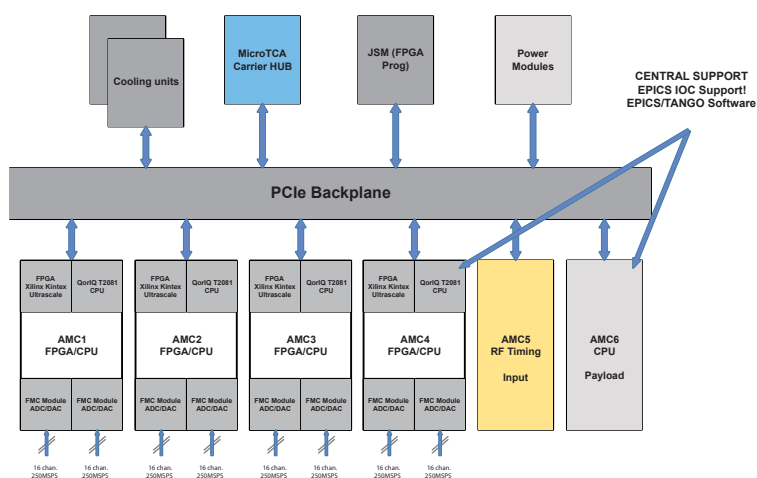


Features

- mTCA.4 Open Standard Standard Compliant System
- EPICS Support on a System Level and EPICS IOC
- Redundant Modular PSU Option
- Flexible Choice of Payload X86 CPU
- Uses "branch standard" MCH / MicroTCA Carrier Hub
- Uses well known & proven Timing Module
- Up to 4 pcs. Digitizers can be installed
- Choice of different FPGA performance level's
- RTM and / or FMC Modules used for Digitizer process adaptation

Block diagram

MicroTCA .4 System for BLM, BPM or LLRF



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Technical specifications

- Dimensions:
Weight: 10-12 kg
(Configuration dependent)
- Configuration:
Power consumption: 3-400 W typical
(Configuration dependent)
Power Supply: 110-265 VAC input
- Environmental:
Operating temp: -25 °C to +55 °C
Storage Temp: -40 °C to +80 °C
Relative Humidity: 5 to 95 percent, non-condensing
- Payload:
CPU: Double module AMC Processor
3rd gen Intel® Core™ Processor
Optional µRTM
Storage: Solid State modules
OS Support: Linux®, Windows®, VxWorks®
- µTCA Carrier Hub (MCH):
Management for: 6 AMCs
Front and rear cooling units
2x Power Modules (N+1 redundancy)
GigaBit Ethernet Switching
PCI Express (Gen3) Switching
Front Panel Uplinks
- Timing Module: Bitrate 1.0 to 2.5 Gbps,
event clock rate 50MHz to 125MHz
jitter typically < 25 ps rms
- Digitizer: NXP QorIQ T2081 @ 1.8 GHz
with AltiVec Xilinx Kintex
UltraScale FPGA (KU040 / KU060)